

FIG. 1

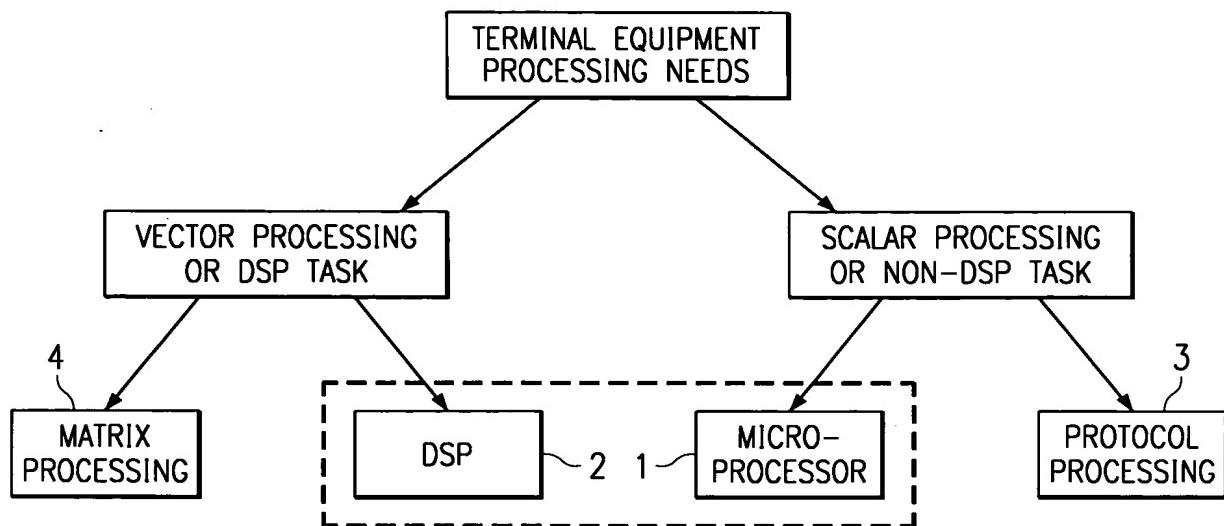


FIG. 2

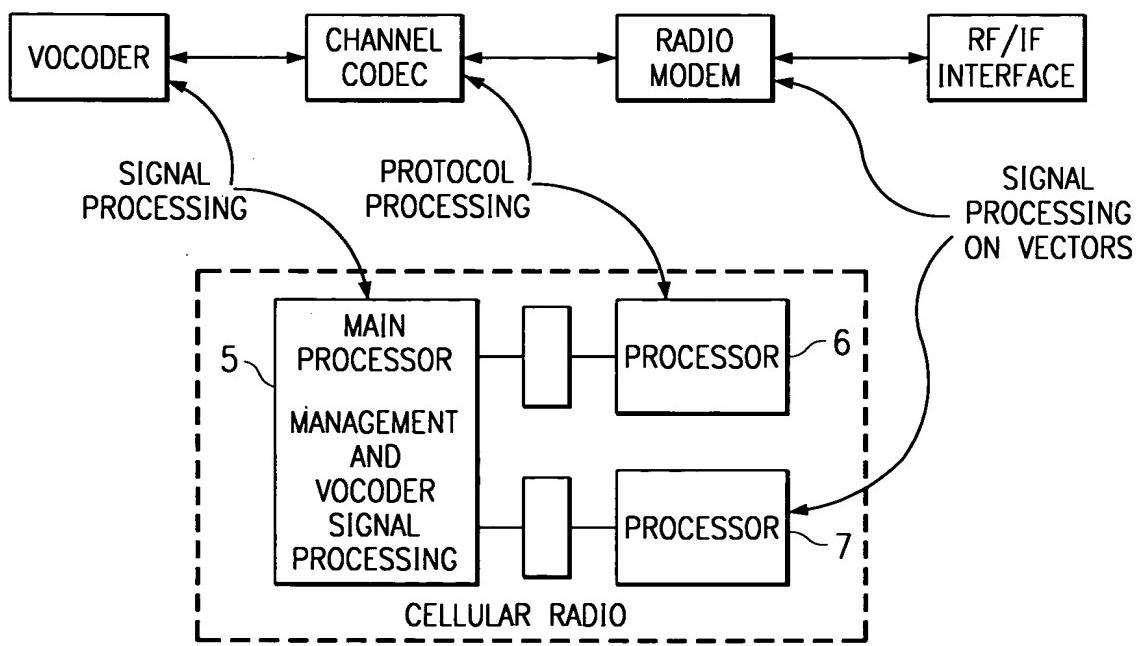


FIG. 3

PERFORMANCE OF CHANNEL CODEC ROUTINES

ROUTINES	DSP UTILIZATION C5x	PROC PROTOCOL PP UTILIZATION
16 BIT CRC IDENTIFICATION	6 INSTR/BIT 5 INSTR/BIT	4 INSTR/BIT 1 INSTR/BIT
RATIO		
SEL/INSTR EFFICIENCY	x1	x2.2
NO. OF TRANS	58 KTx	6.5 KTx
MIPS	28 MIPS	28x2.2=62 MIPS DSP

FIG. 4

PERFORMANCE OF MODEM ROUTINES

ROUTINES	DSP UTILIZATION C5x	ARRAY PROC
METRIC COMPUTATION 57 SYMBOLS (4 SAMPLES)	43800 CYCLES	4400 CYCLES
RATIO		
INSTRUCTION SETTING EFFICIENCY	x1	x10
MIPS	28 MIPS	28x10=280 MIPS DSP

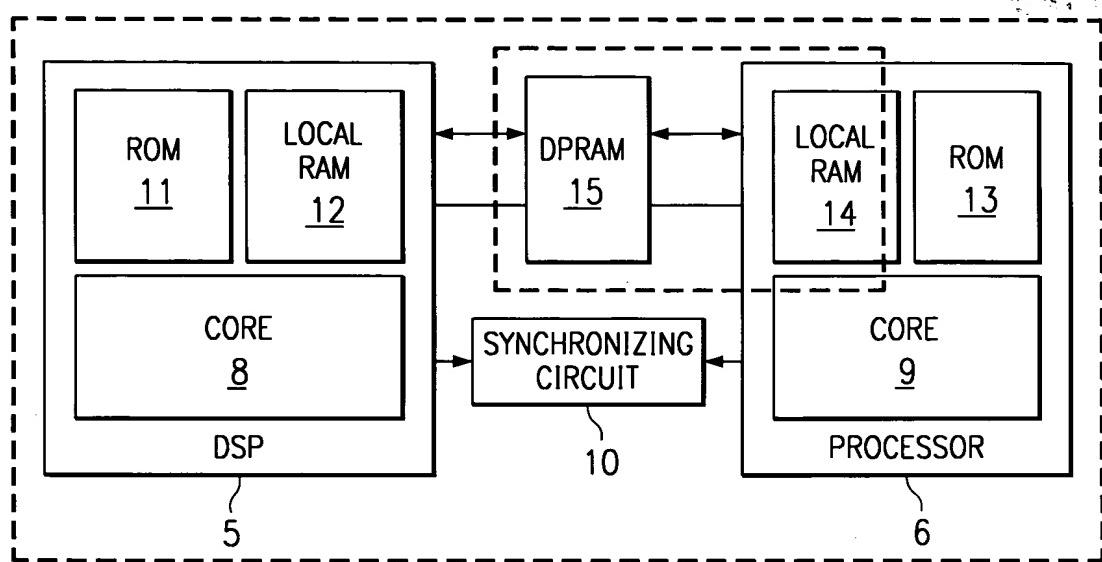


FIG. 5

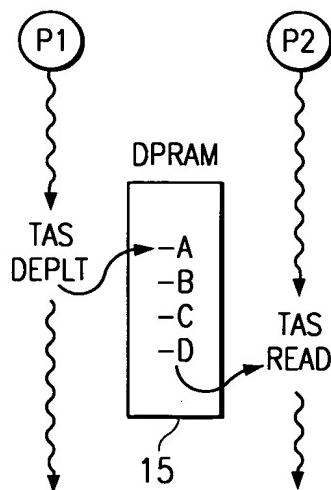


FIG. 6

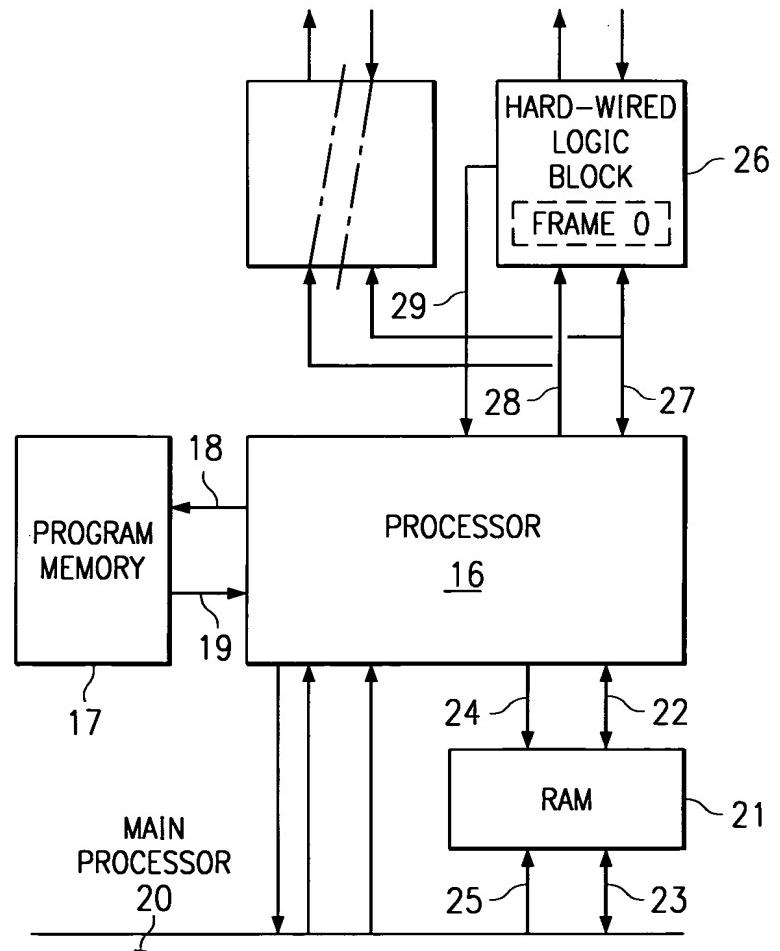


FIG. 7

FIG. 8

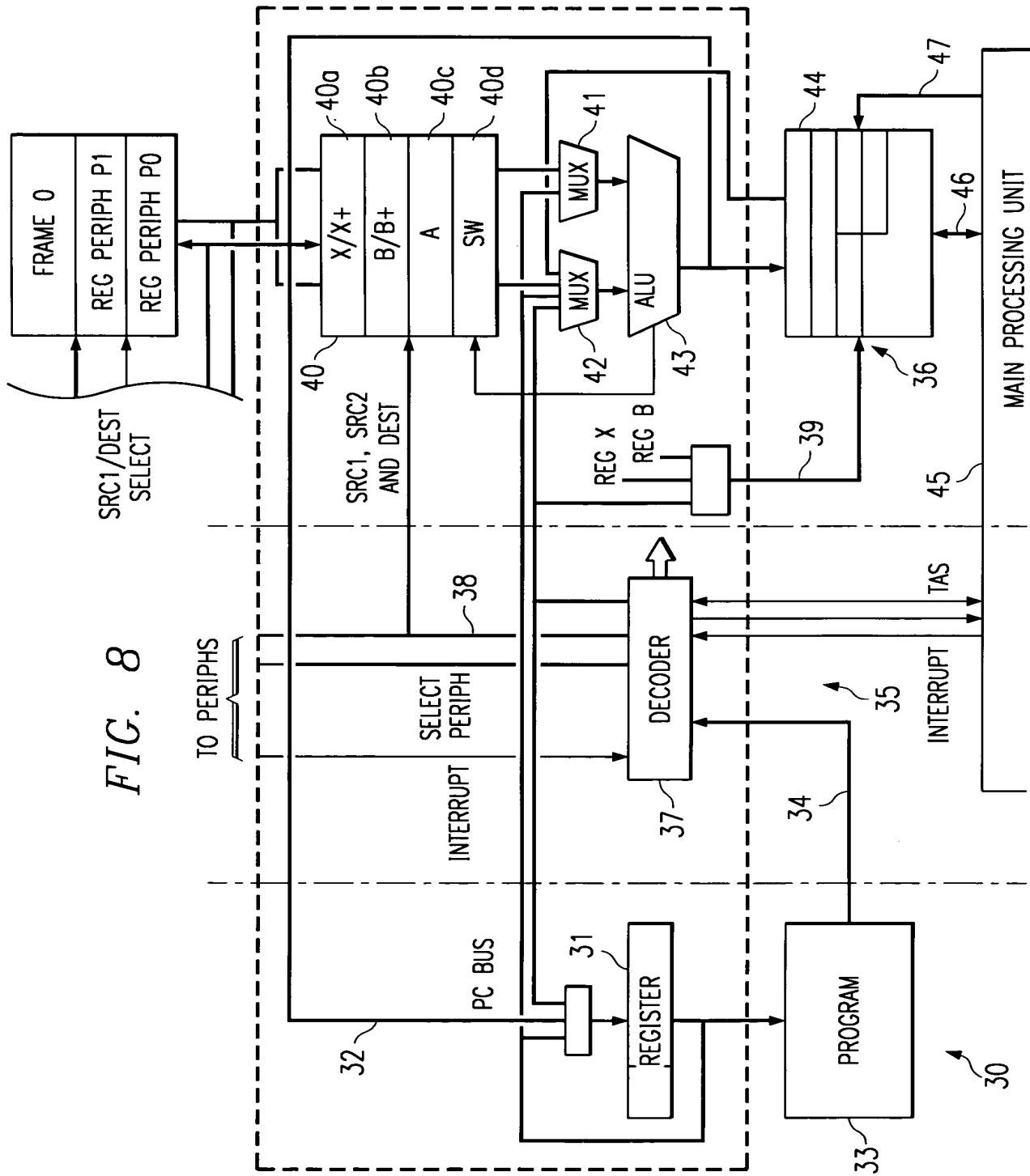


FIG. 9

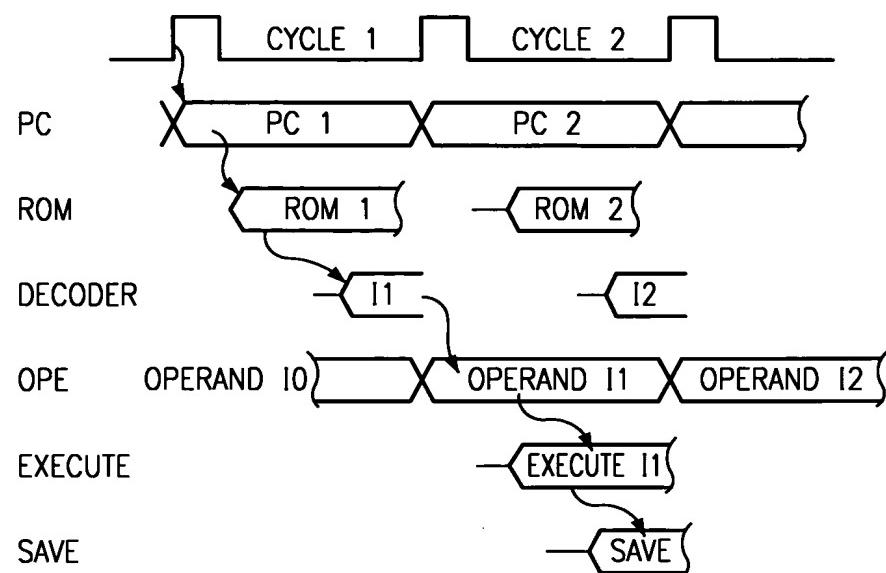
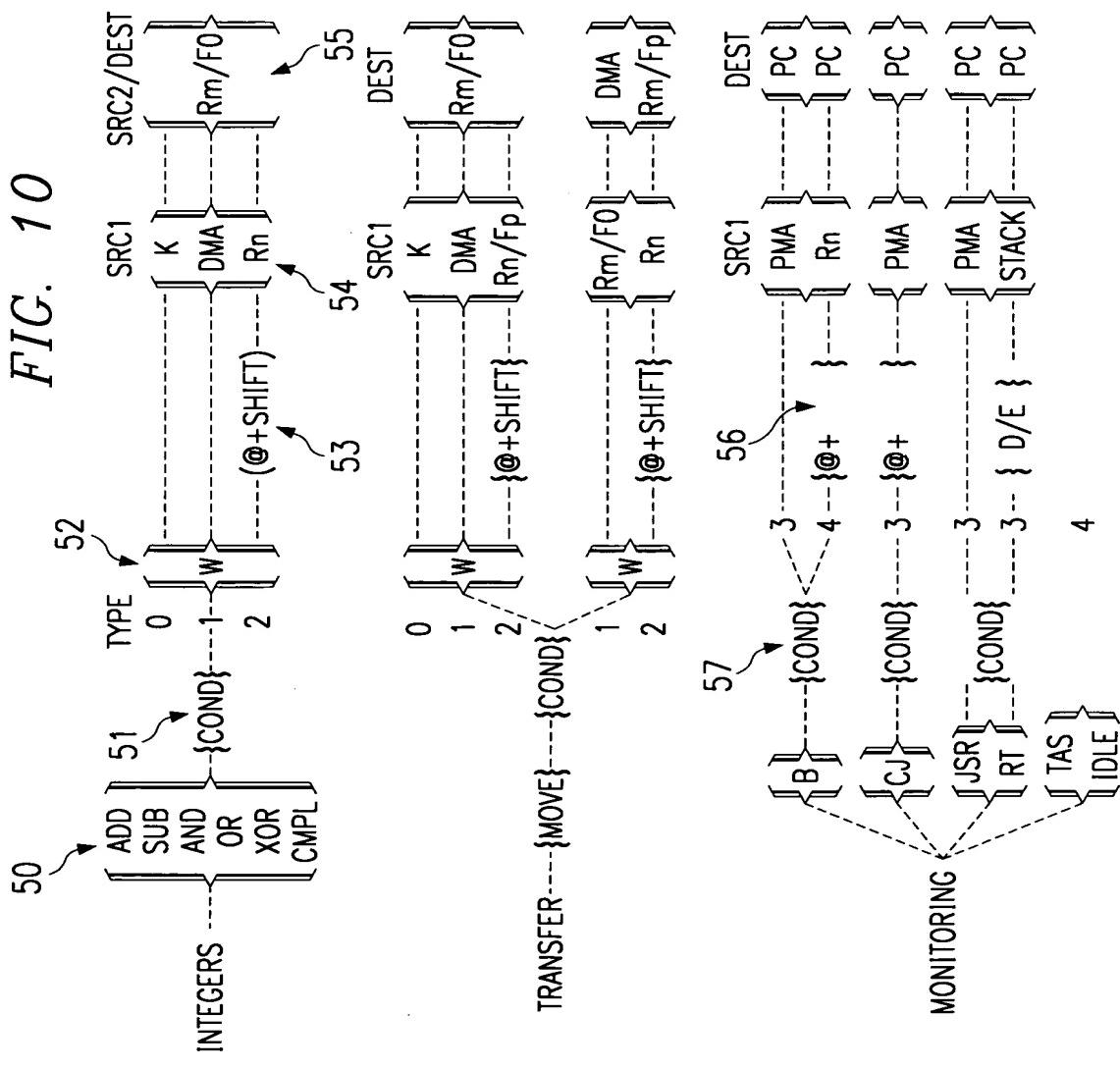


FIG. 11

TYPE	OP CODE												
	0	0	ALU	Cc	1	K	W	Rm	K				
1	0	1	ALU	Cc	1	DMA	W	Rm	DMA				
2	1	0	ALU	Cc	1	@	+	W	Rm	Rn	FP	S	L
3	1	1	CODE	Cc		@	D	W	PMA				
4	1	1	CODE	Cc		@	+	W	Rn				

} INTEGER/
 TRANSFER
 } MONITORING

FIG. 10



10.1

4

FIG. 12

ALU	CODE	Cc
0 - SUB	0 - ST TYPE 1	0 - NEVER
1 - CPL	1 - ST TYPE 2	1 - ALWAYS
2 - ADD	2 - B TYPE 3	2 - Z
3 - AND	3 - B TYPE 4	3 - NZ
4 - OR	4 - CALL	4 - ZD
5 - XOR	5 - RTS	5 - C
6 - PASSA	6 - RTI	6 - NC
7 - SUBC	7 - STOP	7 - USER
		8 - Z12
		9 - LO
		10 - LE
		11 - G
		12 - GE
		13 - NU
		14 - (BL)
		15 -

Rm/Rn	W	L	S
0 - P0	0 - R/W BYTE	0 - Rm LOW	0 - PASS
1 - P1	1 - R/W WORD	1 - Rm HIGH	1 - SRA
2 - A			2 - SLL
3 - B			3 - SR
4 - X		L	
5 - PC			
6 - SW		0 - DMA/Rn LOW	
7 - SP		1 - DMA/Rn HIGH	

FIG. 14

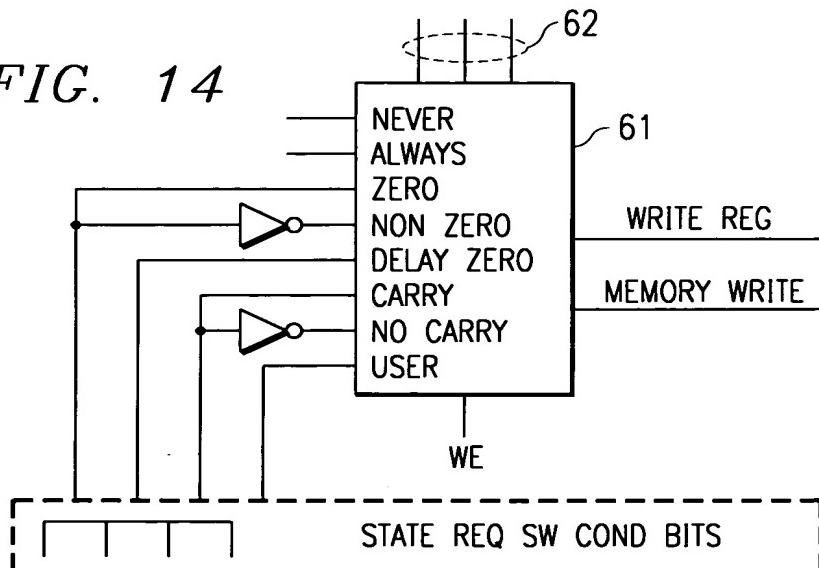


FIG. 13

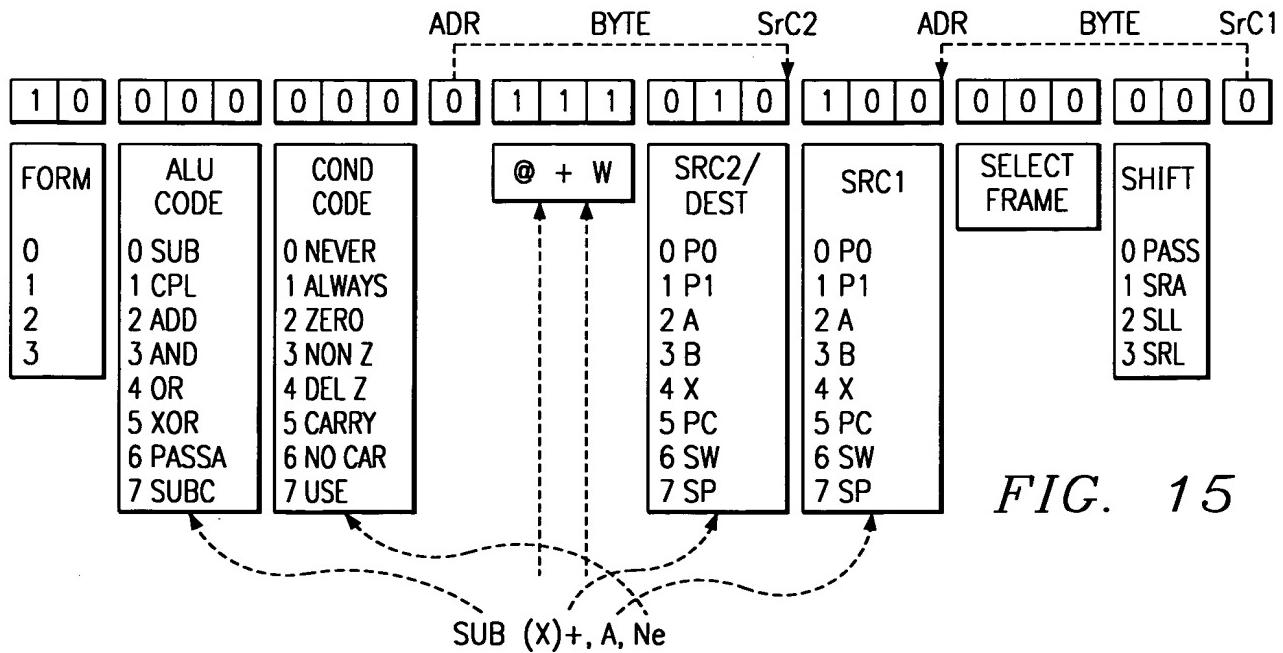
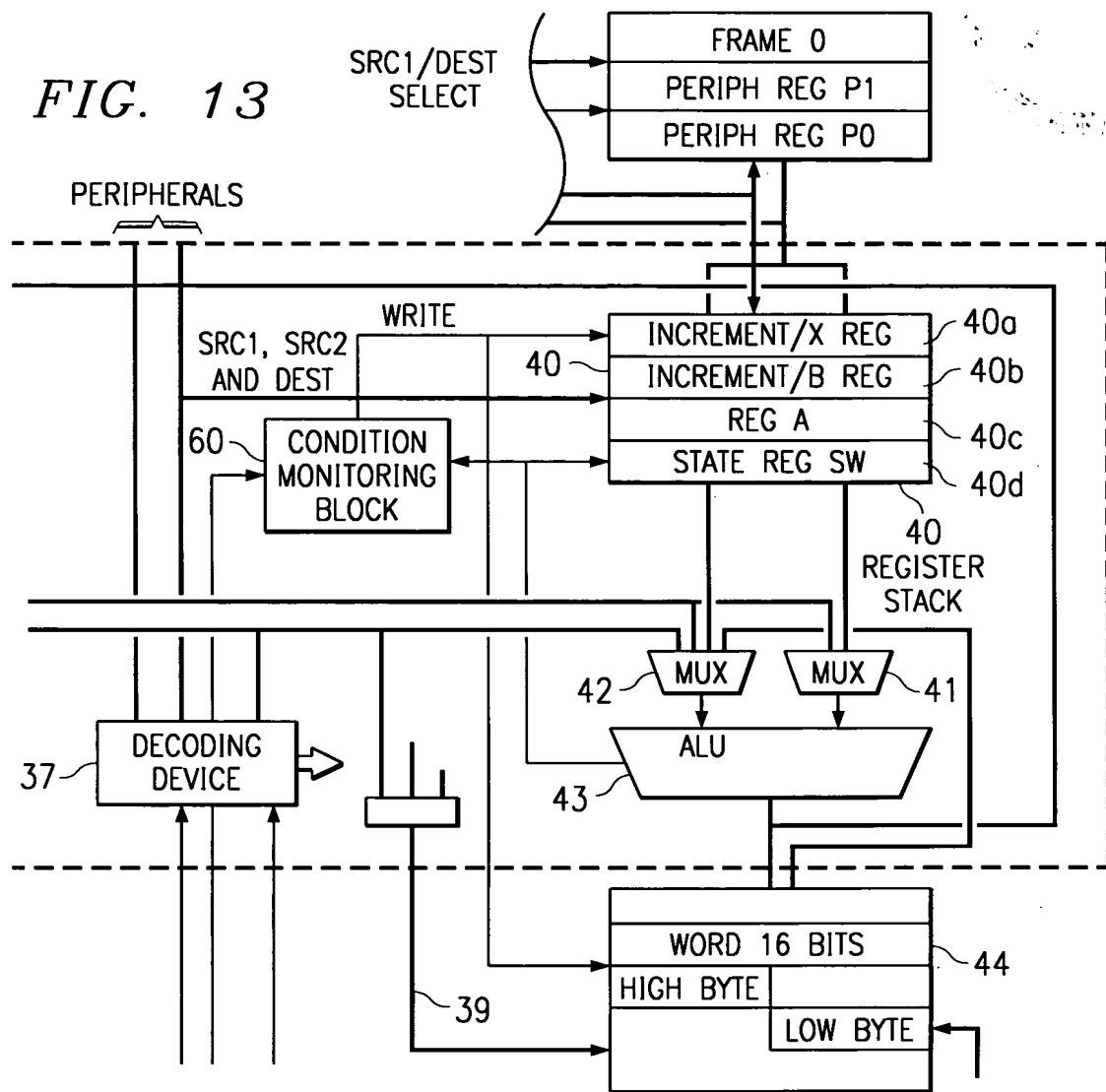


FIG. 15

